

PTO-1449 REPRODUCED  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>February 22, 2000</b>  (Use several sheets if necessary)				ATTORNEY DOCKET NO. DEC99-55 (PD99-2662)		APPLICATION NO. 09/411,194	
				APPLICANT Joel S. Emer et al.			
				FILING DATE October 1, 1999		GROUP 2781	
U.S. PATENT DOCUMENTS							
EXAM- INER INI- TIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
[CSK]	AA	5,790,851	08-04-98	Frank et al. 709/104	<del>395</del>	<del>600</del>	
[G/C]	AB	5,524,247	06-04-96	Mizuno 710/200	<del>395</del>	<del>726</del>	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
	AL						
	AM						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
[G/C]	AR	Tullsen, D. M., et al., "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor," Paper Presented at the Proceedings of the 23 <sup>rd</sup> Annual International Symposium on Computer Architecture, Philadelphia, PA, (May 1996).					
[G/C]	AS	Lo, J.L., et al., "Converting Thread-Level Parallelism to Instruction-level Parallelism via Simultaneous Multithreading," Version of this Paper to appear in ACM Transactions on Computer Systems, pp. 1-25 (August 1997).					
[G/C]	AT	Tullsen, D.M., et al., "Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor," UCSD CSE Technical Report (CS98-587) and UW CSE Technical Report (UW-CSE-98-06-02) (June 1998).					
[G/C]	AU	Tullsen, D. M. et al., "Simultaneous Multithreading: Maximizing On-Chip Parallelism," Paper Presented at the Proceedings of the 22 <sup>nd</sup> Annual International Symposium on Computer Architecture, Santa Margherita Ligure, Italy (June, 1995).					
[G/C]	AV	Eggers, S. J. et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," IEEE Micro, pps. 12-19, (September/October 1997).					
EXAMINER Ken S. Kim				DATE CONSIDERED 4-22-02			

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